nPM1304

Revision 2

Errata

v1.0

NORDIC® SEMICONDUCTOR

Contents

1 nPM1304 Revision 2 Errata

This Errata document contains anomalies and configurations for the nPM1304 PMIC, Revision 2 (QEAA-B00, CAAA-B00).

The document indicates which anomalies are fixed, inherited, or new compared to Revision 1.

2 Revision history

See the following list for an overview of changes from previous versions of this document.

Version	Date	Change
nPM1304 Revision 2 v1.0	11.03.2026	• Added: No. 5. “Programming BUCK voltage increases current consumption” • Added: No. 6. “BUCK mode transition is outside of specification”

3 New and inherited anomalies

The following anomalies are present in Revision 2 of the nPM1304 PMIC.

ID	Module	Description	Inherited from Revision 1
5	BUCK	Programming BUCK voltage increases current consumption	X
6	BUCK	BUCK mode transition is outside of specification	X

Table 1: New and inherited anomalies

3.1 [5] BUCK: Programming BUCK voltage increases current consumption

This anomaly applies to Revision 2, build codes QEAA-B00, CAAA-B00.

It was inherited from the previous PMIC revision Revision 1.

Symptoms

Quiescent current of BUCK is higher than expected.

Conditions

Host software sets BUCK voltage for the first time after a power-up event, and the value is the same as the voltage already set by VSET resistor. This is done by setting BUCKnSWCTRLSEL to SWCTRL while BUCKnNORMVOUT and BUCKnVOUTSTATUS are equal.

Consequences

BUCK quiescent current increases by 1 mA.

Workaround

Host software must initially ensure BUCKnNORMVOUT and BUCKnVOUTSTATUS are not equal when BUCKnSWCTRLSEL is set. Host software can set BUCKnNORMVOUT equal to BUCKnVOUTSTATUS once an alternate BUCKnNORMVOUT has been set.

3.2 [6] BUCK: BUCK mode transition is outside of specification

This anomaly applies to Revision 2, build codes QEAA-B00, CAAA-B00.

It was inherited from the previous PMIC revision Revision 1.

Symptoms

BUCK changes mode from Hysteretic to PWM or from PWM to hysteretic at a load current that deviates from typical specification. Increased output voltage ripple or increased quiescent current might be seen as a result of this.

Conditions

BUCK is enabled in AUTO mode. When input voltage is above 4.2 V and output voltage is set below 1.4 V, PWM to Hysteretic transition level has an increased spread. For high input voltages and high output voltages, Hysteretic to PWM transition can happen at a lower load current than expected.

Consequences

When BUCK is in PWM instead of being in Hysteretic mode, BUCK quiescent current can increase by approximately 4 mA causing lower efficiency at light load currents. When BUCK is toggling between PWM and Hysteretic modes, increased output voltage ripple might be observed.

Workaround

Force hysteretic or PWM mode through TWI or through a configured GPIO from host software. Choose mode to maximize efficiency.

4 Fixed anomalies

The anomalies listed in this table are no longer present in the current PMIC version.

For a detailed description of the fixed anomalies, see the Errata for Revision 1.

ID	Module	Description
4	LOADSW/LDO	LDO startup causes reset
7	LOADSW/LDO	LDO startup time exceeds specification

Table 2: Fixed anomalies